

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|-------|--|--|------------------|---------|------------------|
| L1 | 28098 | @rlad<"20000607" and ((design\$3 or IP or model) and (access with (privilege or control))) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/19 10:10 |
| L2 | 60 | L1 and ((on-line or online) with bid\$4) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/19 10:11 |
| L3 | 1026 | @rlad<"20000607" and (ISS or (instruction adj set adj simulator) and debugger) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/19 12:17 |
| L4 | 1 | L3 and (publish\$3 and (match\$3 adj engine)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/19 12:18 |
| L11 | 1759 | @rlad<"20000607" and (design\$3 and system and simulation and (SOC or (system adj on adj chip))) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/19 12:28 |
| L12 | 16 | L11 and (develop\$3 with hardware) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/19 12:32 |
| L13 | 15 | L11 and (prototyp\$3 with hardware) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/19 12:33 |
| S1 | 29096 | @rlad<"20000607" and (design\$3 and system and (SOC or (system adj on adj chip))) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/16 14:49 |
| S2 | 79072 | @rlad<"20000607" and (design\$3 and system and simulation (SOC or (system adj on adj chip))) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 11:52 |
| S3 | 1759 | @rlad<"20000607" and (design\$3 and system and simulation and (SOC or (system adj on adj chip))) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 11:52 |
| S4 | 308 | S3 and (processor and instruction) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 16:46 |
| S5 | 65 | S3 and ((embed\$3 with system) and instruction) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 12:04 |

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| S6 | 65870 | "13" and (embed\$3 and instruction) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 12:05 |
| S7 | 2156 | S1 and (embed\$3 and instruction) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 12:15 |
| S8 | 3316 | S1 and simulat\$3 | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 12:14 |
| S9 | 6105 | S1 and (simulat\$3 or verificat\$3 or validat\$3) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 12:15 |
| S10 | 917 | S9 and (embed\$3 and instruction) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 12:16 |
| S11 | 524 | S10 and (processor or core) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 14:29 |
| S12 | 421 | S11 and library | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 14:20 |
| S13 | 64 | S12 and S11 and debug\$4 | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 14:18 |
| S14 | 54 | S12 and (FSM or (state adj machine)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 14:20 |
| S15 | 123 | S11 and IP and access | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 14:48 |
| S16 | 148 | S11 and (IP and (access\$3 or authoriz\$3 or authorization)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 14:48 |
| S17 | 148 | S16 and (user or group or vendor) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 14:49 |

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| S18 | 103 | S12 and IP and access | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 16:14 |
| S19 | 119 | S12 and (IP and (access\$3 or authoriz\$3 or authorization)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 14:48 |
| S20 | 119 | S19 and (user or group or vendor) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/13 14:49 |
| S21 | 1 | 09/872435 | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 16:34 |
| S22 | 1 | "5197016".pn. | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 16:34 |
| S23 | 1759 | @rlad<"20000607" and (design\$3 and system and simulation and (SOC or (system adj on adj chip))) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/19 12:27 |
| S24 | 22 | S23 and (ISS or (instruction adj set adj simulator) and debugger) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 19:18 |
| S25 | 3 | S23 and ((ISS or (instruction adj set adj simulator)) and debugger) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 16:47 |
| S26 | 129 | S23 and (stor\$3 and server) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 19:24 |
| S27 | 22 | S26 and (access with (priviledge or control\$3)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 17:04 |
| S28 | 208 | S23 and (IP and access) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 16:57 |
| S29 | 41 | S28 and (stor\$3 and server) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 16:58 |

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| S30 | 28083 | @rlad<"20000607" and ((design\$3 or IP or model) and (access with (privilege or control)))) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 17:03 |
| S31 | 22 | S26 and (access with (privilege or control\$3)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 17:03 |
| S32 | 2 | S31 and (ISS or (instruction adj set adj simulator) and debugger) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/15 15:07 |
| S33 | 123 | S23 and (access with (privilege or control\$3)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 17:05 |
| S34 | 124 | S23 and (access with (right or privilege or control\$3)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 17:06 |
| S35 | 89 | S23 and (version with (control\$4 or privilege or right)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 19:22 |
| S36 | 2228 | S30 and (version with (control\$4 or privilege or right)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 19:22 |
| S37 | 1137 | S36 and (stor\$3 and server) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 19:24 |
| S38 | 905 | S37 and (user with access) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 19:25 |
| S39 | 469 | S38 and library | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/14 19:25 |
| S40 | 1026 | @rlad<"20000607" and (ISS or (instruction adj set adj simulator) and debugger) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/15 15:16 |
| S41 | 61 | S40 and (GUI or (graphical adj user adj interface)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/19 12:17 |

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| S42 | 13 | @rlad<"20000607" and ((instruction adj set adj simulator) and debugger) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/15 15:16 |
| S43 | 9 | S42 and (GUI or (graphical adj user adj interface)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/16 13:36 |
| S44 | 4 | "0772140" | US-PGPUB; USPAT; USOCR; EPO; IBM_TDB | OR | ON | 2005/12/16 14:48 |
| S45 | 402 | cadence.as. | US-PGPUB; USPAT; USOCR; EPO; IBM_TDB | OR | ON | 2005/12/16 14:48 |
| S46 | 62 | @rlad<"20000607" and cadence.as. | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/16 14:52 |
| S47 | 5 | @rlad<"20000607" and ((design adj manager) and (version with control\$4)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/16 14:59 |
| S48 | 5 | S47 and access | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/16 14:55 |
| S49 | 1 | S48 and privilege | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/16 14:56 |
| S50 | 2 | S48 and edit\$4 | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/19 10:10 |
| S51 | 2 | S50 and (grant\$3 or provid\$3) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/16 14:58 |
| S52 | 0 | @rlad<"20000607" and ((design adj manager) and (online with bid\$3)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/16 15:00 |
| S53 | 0 | @rlad<"20000607" and ((design adj manager) and (on-line with bid\$3)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/16 15:00 |

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| S54 | 1 | @rlad<"20000607" and ((design adj manager) and (on-line with bid\$4)) | US-PGPUB; USPAT; USOCR; IBM_TDB | OR | ON | 2005/12/16 15:00 |
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